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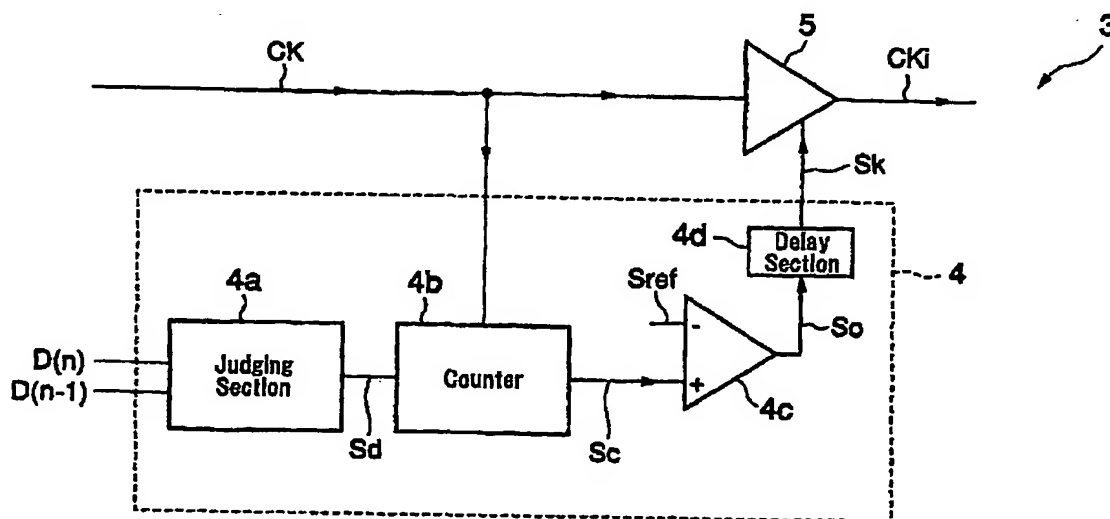
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- (71) Applicant (for all designated States except US): KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): NEGISHI, Nobuji [JP/JP]; c/o Philips Japan, Ltd., Philips Bldg., 2-13-37, Kohnan, Minato-ku, Tokyo 108-8507 (JP). KISHIDA, Masaya [JP/JP]; c/o Philips Japan, Ltd., Philips Bldg., 2-13-37, Kohnan, Minato-ku, Tokyo 108-8507 (JP).
- (74) Agent: AOKI, Hiroyoshi; c/o Philips Japan, Ltd., Philips Bldg., 2-13-37, Kohnan, Minato-ku, Tokyo 108-8507 (JP).
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[Continued on next page]

(54) Title: CIRCUIT ARRANGEMENT



(57) Abstract: An object of the present invention is to provide a circuit device in which the power consumption can be reduced without the dedicated signal. A circuit device (1) comprising a D flip-flop (F0) for receiving a pulse of a clock signal (CK) to introduce data therein and output said introduced data and a shift register (2), comprising the D flip-flops (F1 to F7) for introducing the data therein in accordance with the pulse to output the introduced data, for processing the outputted data from the D flip-flop (F0), wherein the circuit device (1) comprises a control circuit (3) for controlling whether the D flip-flops (F1 to F7) are supplied with the pulse of the clock signal (CK) on the basis of outputted data from the D flip-flop (F0) in accordance with the pulse of the clock signal (CK) and data to be introduced into the D flip-flop (F0) in accordance with the next pulse.

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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER
 IPC 7 H03K5/13 G11C19/28

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	EP 0 703 663 A (SGS THOMSON MICROELECTRONICS) 27 March 1996 (1996-03-27) figure 4	1,4,6,9
X	EP 0 524 712 A (SHARP KK) 27 January 1993 (1993-01-27) figure 4	1,6,9
X	US 5 883 529 A (KUMATA ICHIRO ET AL) 16 March 1999 (1999-03-16) figure 31	1-3

☐ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
 NL - 2280 HV Rijswijk
 Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
 Fax: (+31-70) 340-3016

Authorized officer

Brown, J

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